



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Richard Alan Hamersley

Title: PROCESSING OF PROCESSOR PERFORMANCE STATE
INFORMATION

Application No.: 10/757,273

Filed: January 14, 2004

Examiner: Paul B. Yanchus III

Group Art Unit: 2116

Atty. Docket No.: 1001-0242

Confirmation No.: 4547

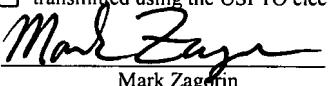
May 22, 2007

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Commissioner for Patents
P.O. Box 1450
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**NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF
PATENT APPEALS AND INTERFERENCES**

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the last decision of the Examiner.

The fee for this Notice of Appeal under 37 C.F.R. § 41.20(b)(1) is submitted as set forth in the accompanying transmittal letter.

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 Mark Zagorin	<u>5/22/07</u> Date

Respectfully submitted,



Mark Zagorin, Reg. No. 36,067
Attorney for Applicant(s)
(512) 338-6311 (direct)
(512) 338-6300 (main)
(512) 338-6301 (fax)

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05/30/2007 HGBREM1 00000036 10757273

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